

ABSTRACT OF THE DISCLOSURE

A method and apparatus for driving a plasma display panel (PDP) by which reset stabilization can be achieved are provided. In the method for driving a PDP, successive field periods, each including a reset period for initializing the state of respective cells, an address
5 period for selectively discriminating cells to be turned on from cells not to be turned on and for performing an addressing operation, and a sustain period for discharging the addressed cells are performed. A reset stabilization period for inducing discharging in a discharge space between cells is additionally performed before the reset period if a rest period having a predetermined
10 time duration is present between the sustain period of a preceding field and the reset period of the field. Therefore, the reset operation can be performed in a state where discharge cells between electrodes are sufficiently primed.

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